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APPLICANTS

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This application is a CON of 09/964,341 09/28/2001 PAT 6,686,108

Cong ** FOREIGN APPLICATIONS ****

JAPAN 2000-313513 10/13/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/14/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	25	18	1
Verified and Acknowledged	<i>Cong</i> Examiner's Signature Initials				

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TITLE

Fabrication method of semiconductor integrated circuit device

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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